

Config. program V1.00
Firmware for BDI2000 V1.01
Logic for BDI2000 V1.05

ABATRON

Date: January 21, 2011
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Enhancements

- Support for Reset Configuration Word (RCW) override added (see manual).
- Some GDB protocol improvements.

Config. program V1.00
Firmware for BDI2000 V1.02
Logic for BDI2000 V1.05

ABATRON

Date: May 13, 2011
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Enhancements

- Support for P5020 and P3041 added.
- Interrupts are disabled via EBCR0 during single step (not supported for e500mc rev.1).

Config. program V1.00
Firmware for BDI2000 V1.03
Logic for BDI2000 V1.05

ABATRON

Date: January 9, 2012

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Enhancements

- Support for Spansion S29GL-S flash added (S29GLSX16 algorithm)
- Add MMU PTE64 option.

Error Correction

- Save and restore all MASx registers when reading TLB content.

Config. program V1.00
Firmware for BDI2000 V1.04
Logic for BDI2000 V1.05

ABATRON

Date: June 6, 2012
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Enhancements

- Support for Spansion S29VS-R flash added (S29VSRX16 algorithm)
- Time-out time when waiting for reset released by the target has been increased to 1 second.
- New CPUTYPE 5040 and 5021 accepted.
- The WREG init list entry now accepts all names from the register definition file.
- The new Telnet command „update“ allows to reload the BDI configuration and register definition file without rebooting the BDI.
- GDB: The BDI now answers with 1 to the GDB qAttached packet.

Error Correction

- A CCSRBAR update by the user via Telnet or init list is now also recognized by the BDI and updates the BDI local copy of CCSRBAR.

Config. program V1.00
Firmware for BDI2000 V1.05
Logic for BDI2000 V1.06

ABATRON

Date: March 21, 2013
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Enhancements

- The JTAG clock frequency can be entered in Hertz and not only as an index.
- Maximal Telnet and GDB remote command length increased to 256 characters.
- MMU translation supports now also 64-bit effective addresses.
- Initial support for T4 (e6500) added.
- New Telnet commands to access e6500 vector registers added.
- New Telnet command „exec“ allows to execute a single PPC instruction.
- Experimental support for „Non-stop Multi-Threaded Debugging in GDB“ added (see manual).
- For flash algorithm MIRRORX16 and S29GLSX16 the Telnet unlock command erases the PPB bits.
- The sector erase function for S29GLSX16 uses the S29GL-S Blank Check command to test if a sector is already erased. The erase command is only execute if the sector is not already blank.

Error Correction

- A too long GDB remote command (monitor command) no longer crashes the BDI.
- Memory access works now also when there are additional devices on the JTAG scan chain.

Config. program V1.00
Firmware for BDI2000 V1.06
Logic for BDI2000 V1.06

ABATRON

Date: Sept. 23, 2013
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Enhancements

- Support for SDC challenge/response operation added (see manual).
- New option allows to select SNOOP or NOSNOOP for SAP memory accesses.
- New CGROUP parameter allows to start multiple cores via GDB continue command (see manual).
- Maximal number of ELF program headers increased to 64.
- Writes to DCSR space are also executed for running cores. This allows to setup the Cross-Trigger logic of the processor even when "reset run" or "STARTUP RUN" is used.
- In order to allow access to the whole DCSR address range the base address has changed from 0x00020000_00022000 to 0x00020000_00000000. If already in use, the offsets to DCSR registers must be updated. For example: CGCR0 from 0x204 to 0x22204.

Config. program ■ V1.02
Firmware for BDI2000 ■ V1.07
Logic for BDI2000 ■ V1.06

ABATRON

Date: March 20, 2014
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Enhancements

- Support QorIQ T1 and T2 processor family added.
- Configuration and register definitions can be stored in the BDI internal Flash memory. In this case no TFTP server is necessary to load the configuration files.

Config. program V1.02
Firmware for BDI2000 V1.08
Logic for BDI2000 V1.06

ABATRON

Date: Sept. 29, 2014
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Enhancements

- Display of T4xxx, T2xxx and T1xxx L1/L2 cache content added.
- Display of instruction cache content shows now the original (not pre-decoded) opcode value.
- New Telnet QUERY command to display parts of the configuration.

QUERY [<core>]

Examples:

BDI> query displays common and core specific configuration for all cores

BDI> query 1 displays the core specific configuration of the selected core

Config. program V1.02
Firmware for BDI2000 V1.09
Logic for BDI2000 V1.06

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Date: Sept. 15, 2015
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Enhancements

- Allow to set more DBCR0 bits when host controlled break/watch.
- New Telnet DBCR0 command allows to set additional DBCR0 bits:
DBCRO [IRPT][TRAP][RET][CIRPT][CRET]

Error Correction

- Display correct step mode in QUERY command.