

Config. program ☒ V1.01
Firmware for BDI2000 ☒ V1.01
Logic for BDI2000 ☐ V1.06

ABATRON

Date: January 6, 2006
Page: 1/1

Enhancements

- Telnet history buffer no longer filled with repeated commands.
- The Windows setup tools now allows to select a COM port in the range 1 - 19.
- The Telnet CONFIG command displays the firmware version.

Error Correction

- Correct single step of the Thumb BX pc instruction.
- Correct handling of ARM/Thumb mode switch while in debug state.

Config. program ☐ V1.01
Firmware for BDI2000 ☒ V1.02
Logic for BDI2000 ☐ V1.06

ABATRON

Date: June 9, 2006

Page: 1/1

Enhancements

- Enhanced flash erase support via configuration file (see user's manual).
- Improved Telnet history buffer handling.
- Support for low level scan chain configuration added (see user's manual).

Error Correction

- Correct display of banked registers (rdall command).

Config. program ☐ V1.01
Firmware for BDI2000 ☒ V1.03
Logic for BDI2000 ☐ V1.06

ABATRON

Date: June 22, 2007

Page: 1/1

Enhancements

- New flash algorithm types added (S29M64X8, S29M32X16).
- The MirrorBit algorithm uses the write buffer also when no workspace is defined.
- New RECONNECT option to the DEBUGPORT parameter added. When present, a Telnet/GDB (TCP) connection request to an already open TCP channel is accepted if the host IP is the same as the initial one. The old TCP channel will be closed immediately.
- Telnet accepts now multiple commands separated by a semi-colon on one line.

Config. program ☒ V1.02
Firmware for BDI2000 ☒ V1.04
Logic for BDI2000 ☐ V1.06

ABATRON

Date: Oct 28, 2007

Page: 1/1

Enhancements

- Support for Cortex-A8 (OMAP3430) added.

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.05
Logic for BDI2000 ☐ V1.06

ABATRON

Date: January 3, 2008
Page: 1/1

Enhancements

- The new CLOCK init list entry allows to change the JTAG clock frequency during processing of the init list. Maybe useful to switch to a faster clock as soon as possible.
- Improved memory access for Cortex-A8
- Improved OMAP3430 reset sequence.

Error Correction

- Error in I28BX16 target programming algorithm corrected. Programming with workspace was very slow because the algorithm did not enter debug mode after terminating.
- ARM11: The displayed cache TAG values are now correct.
- Cortex-A8: The RESET signal is now released at the end of the standard reset sequence.

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.06
Logic for BDI2000 ☐ V1.06

ABATRON

Date: April 10, 2008

Page: 1/1

Error Correction

- Cortex-A8: Correct fast/stalled memory access sequences.

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.07
Logic for BDI2000 ☐ V1.06

ABATRON

Date: July 9, 2008

Page: 1/1

Enhancements

- Support for ARM1176 and Cortex-M3 (STM32) added.
- Support for STM32F10xx internal flash programming added.

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.08
Logic for BDI2000 ☐ V1.06 / 1.00

ABATRON

Date: Aug. 29, 2008

Page: 1/1

Enhancements

- Support for Luminary Micro Stellaris LM3S internal flash programming added.
- Support for Cortex-M3 / Cortex-A8 Single Wire Debug Mode added (different firmware).

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.09
Logic for BDI2000 ☐ V1.06 / 1.00

ABATRON

Date: Febr. 25, 2009

Page: 1/1

Enhancements

- Support for Cortex-R4 added
- Support for ARM7 connected via JTAG-AP added (see manual).
- New parameter to select the single step mode added (see manual).
- New Telnet commands INTENA and INTDIS added.

Error Correction

- Correct single step of newer ARMv6/v7 instructions.

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.10
Logic for BDI2000 ☐ V1.06 / 1.00

ABATRON

Date: Sept. 29, 2009

Page: 1/1

Enhancements

- Support for DAP-Lite module added.
- Support for multiple A8/A9 debug components behind one DAP added.
- Support for searching nested ROM tables added.
- APB base address of the debug component can be preset (no ROM table search).
- Download speed via GDB improved.
- Support for a different reset type for the initial power-up reset added (see manual).

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.11
Logic for BDI2000 ☒ V1.06 / 1.01

ABATRON

Date: May 17, 2010

Page: 1/1

Enhancements

- Support for Atmel SAM3U / SAM3S internal flash programming added.
- SWD: Single Wire Output can be routed to a TCP/IP channel (see manual).

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.12
Logic for BDI2000 ☐ V1.06 / 1.01

ABATRON

Date: August 20, 2010

Page: 1/1

Enhancements

- Support for Cortex-M0 added.
- Support for LPC1xxx flash programming added.

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.13
Logic for BDI2000 ☐ V1.06 / 1.01

ABATRON

Date: Dec. 23, 2010

Page: 1/1

Enhancements

- Support for STM32L15 internal flash programming added.
- Support for EFM32 internal flash programming added.
- A8: Correct handling of synchronous watchpoints (step over during a restart).
- Get actual number of implemented break/watch points from DIDR register.

Error Correction

- M3: Correct sequence to set debug control bits (e.g. C_MASKINTS) before resume.
- A8/R4: Correct sequence to set PC before resume (use mov pc,r0 instead of bx r0).

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.14
Logic for BDI2000 ☐ V1.06 / 1.01

ABATRON

Date: April 1, 2011
Page: 1/1

Enhancements

- Support for Cortex-M4 added.
- Support for Freescale Kinetis internal flash programming added.
- New EXEC init list entry added (see manual).
- Special DAPPC configuration parameter added for TI processors (see manual).
- New Telnet MMU command allows to disable / enable the MMU.
- New PMM (Physical Memory Mapped) register type accesses memory always with disabled MMU.
- The BDI no longer automatically steps over a GDB set breakpoint at the current PC. This allows to use the GDB „jump“ command.

Error Correction

- A8/R4: Correct setting of instruction HWBP (Byte Select in BCR) for Thumb / Big Endian code.

Config. program ☐ V1.02
Firmware for BDI2000 ☒ V1.15
Logic for BDI2000 ☐ V1.06 / 1.01

ABATRON

Date: Nov. 29, 2011

Page: 1/1

Enhancements

- Support for STM32F2 and STM32F4 internal flash programming added.