

Config. program  V1.00  
Firmware for BDI2000  V1.02  
Logic for BDI2000  V1.01



Date: January 15, 2000  
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## Enhancements

- GATEWAY: HDLC connection between BDI and target supported (see manual).
- GATEWAY: UART and HDLC example driver for MPC8260 added.
- AGENT: Data cache flushed before accessing target memory (see manual).
- AGENT: Option to catch unhandled exception added (see manual).

## Error Correction

- PPC603e: DTAG's no longer corrupted during COP scan's.

Config. program  V1.01  
Firmware for BDI2000  V1.03  
Logic for BDI2000  V1.01



Date: March 24, 2000  
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## Enhancements

- Support for host communication via network gateway added.
- Download speed improved.
- Load offset added to Telnet LOAD, VERIVY and PROG command.

Config. program  V1.01  
Firmware for BDI2000  V1.04  
Logic for BDI2000  V1.01



Date: April 4, 2000  
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## Enhancements

- Registers can be defined and access by name via Telnet (see manual).
- Support for IBM PowerPC 750 Lonestar Rev.3 added.
- Support for Motorola PowerPC 740P/750P Conan/Doyle added.

Config. program  V1.01  
Firmware for BDI2000  V1.05  
Logic for BDI2000  V1.01



Date: April 4, 2000  
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## Enhancements

- It is now possible to define a maximal transfer size for different address ranges (see manual)
- Default register names for MSR, CR and FPSCR has been added.
- New configuration parameter CPUYPE added (see manual).
- Support for MPC755 added.
- Register values can be swapped (see manual).
- Optimized reset sequence that can handle targets where HRESET also forces TRST.

## Error Correction

- For MPC8240/8260 targets, the CORE TAP link module is now selected during a reset sequence.

Config. program  V1.01  
Firmware for BDI2000  V1.06  
Logic for BDI2000  V1.01



Date: Sept 19, 2000  
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## Enhancements

- New Telnet command (BREAK) added to display and change current breakpoint mode.
- Automatic load of binary files now also supported.
- Download progress output at Telnet added.
- User selectable Telnet prompt added. The Telnet prompt can be defined in the configuration file and also interactively changed during a Telnet session.
- Memory access delay configuration parameter added. For very slow memory devices and fast clocked CPU's it can be necessary to increase the number of clocks used to access external memory via COP. This is mainly the case when the memory controller is left in its reset configuration with maximal number of wait states active.
- L1 cache coherent memory access added. For systems with only a L1 cache, flushing the data cache is therefore no longer necessary.  
For this kind of targets you may add the line DCACHE NOFLUSH to the configuration file.
- Flash programming support for Atmel AT49 added.
- New flash erase modes support now chip and block erase.
- The flash programming function (PROG) now supports different file formats.
- Support for PowerPC 32bit data bus mode added.

## Error Correction

- If enabled, data cache flush is also executed when software breakpoints are set or cleared. This is necessary when there is a L2 cache in the system.

Config. program  V1.01  
Firmware for BDI2000  V1.07  
Logic for BDI2000  V1.02



Date: October 5, 2000  
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## Enhancements

- Improved JTAG controller logic. JTAG clock configuration parameter has changed.
- Support for IBM PPC750CX added.  
**Note:** Level shifters are needed between the PPC750CX and the BDI2000 COP connector.
- New configuration parameter to define the boot address added.

Config. program  V1.01  
Firmware for BDI2000  V1.08  
Logic for BDI2000  V1.02



Date: Nov 28, 2000  
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## Enhancements

- New Telnet command (CONFIG) added to display current BDI configuration.
- New Telnet command (FLASH) added to change the flash configuration interactively. This may be useful when running a Telnet script to program different flash memory systems on the target. For example if there is a 8bit boot flash and a 32bit main flash system.

## Error Correction

- Appropriate „eieio“ instructions added to the flash programming algorithm to enforce in-order execution of the flash accesses.

Config. program  V1.02  
Firmware for BDI2000  V1.09  
Logic for BDI2000  V1.02



Date: Febr 8, 2001  
Page: 1/1

## Enhancements

- New Telnet command (DUMP) added to upload a binary memory image.
- Support for MPC7400 added.

## Error Correction

- Time-out in flash algorithm increased.  
This was necessary because of the long programming time of Intel Strata flash chips.



Config. program  V1.02  
Firmware for BDI2000  V1.10  
Logic for BDI2000  V1.02



Date: March 22, 2001  
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## Enhancements

- Faster programming for Intel StrataFlash.
- Support for MPC7410 added.
- Support for accessing L2 cache private memory added.
- Special virtual IO added to access the UART from the host in Agent mode (see manual).

## Error Correction

- Error in PPC755 scan chain definition corrected.

Config. program  V1.02  
Firmware for BDI2000  V1.11  
Logic for BDI2000  V1.02



Date: Dec. 10, 2001  
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## Enhancements

- New init list entry added to write a 64bit value. Used to unlock flashes in a 64bit flash system.
- New configuration parameter (STARTUP) added, see user's manual for more information.
- New configuration parameter (PARITY) added, see user's manual for more information.
- The RS232 port of the target can be routed via the BDI to a TCP/IP channel, see user's manual.
- An alternate single step mode has been implemented. The alternate mode uses a hardware break-point to implement single step.

## Error Correction

- The erase time-out for AMD flashes has been increased. This was necessary because of long chip erase time.

Config. program  V1.02  
Firmware for BDI2000  V1.12  
Logic for BDI2000  V1.02



Date: August 16, 2002  
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## Enhancements

- Support for MPC4200 / MGT5100 added.
- Support for additional IBM PPC750CX PVR's added.
- The new Telnet command RDUMP writes the values of all user defined registers to a file on the host.
- New configuration parameter (WAKEUP) added to support targets that need some wake-up time after the BDI releases COP-HRESET.
- A List of valid memory ranges can be defined to avoid invalid memory accesses via the JTAG interface (MMAP entry in the init list).
- Support for IBM PPC750FX added.
- Support for AMD MirrorBit flash added.

## Error Correction

- 750CX: Access to HID1 added.
- MPC82xx: Access to HID2 register corrected.

Config. program  V1.03  
Firmware for BDI2000  V1.13  
Logic for BDI2000  V1.02



Date: January 7, 2003  
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## Enhancements

- Support for MGT5200 and MPC8270/75/80 added.
- Support for data breakpoint via DABR added (Telnet command BD and BDT).
- Support for network configuration via BOOTP added.
- New init list entries allow to execute memory read accesses.
- New Telnet command (DCACHE) allows to display L1 data cache content.
- Support for 32 bit wide flash chips added.
- New Telnet command added to unlock multiple (same size, continuous) flash sectors with one command.
- Improved Telnet „erase“ command to erase multiple (same size, continuous) flash sectors with one command.
- The Windows setup tool is now a WIN32 application.

## Error Correction

- Correct access to L1 data cache for 750FX, 7400 and 74100
- MGT5100: Correct access to BAT registers.

## Note:

The BDI's ethernet MAC address will change with this release.  
The setup tool displays the new MAC address.

Config. program  V1.03  
Firmware for BDI2000  V1.14  
Logic for BDI2000  V1.02



Date: April 1, 2003  
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## Enhancements

- Support for MGT5200 Rev.1.0 completed
- Support for MPC8280/75/70 enhanced
- Improved Telnet interface with history buffer and line editing.

## Error Correction

- Error when requesting BOOTP parameters corrected.

Config. program  V1.03  
Firmware for BDI2000  V1.15  
Logic for BDI2000  V1.02



Date: July 23, 2003

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## Enhancements

- The Telnet command „CONFIG“ allows to change the name of the configuration file name. The new name is stored in the BDI's flash and a boot sequence is started.

## Error Correction

- Error in checksum calculation corrected (Telnet command MC).
- Correct access to the 82xx UPM array via Init list and Telnet.
- Error in MGT5200 support corrected. Some BDI's did not work correct with MGT5200 targets.

Config. program  V1.03  
Firmware for BDI2000  V1.16  
Logic for BDI2000  V1.02



Date: July 30, 2003

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## Error Correction

- Error when programming flash with workspace for MPC5200 and MPC8270/75/80 corrected. This error was newly introduced with the last V1.15 firmware version.

Config. program  V1.04  
Firmware for BDI2000  V1.17  
Logic for BDI2000  V1.02



Date: March 3, 2004  
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## Enhancements

- The file names in the configuration file maybe relative to the configuration file path.
- The Telnet command „CONFIG“ allows to change the name and IP address of the configuration file. The new name and IP address is stored in the BDI's flash and a boot sequence is started.
- Support for multiple devices on the JTAG scan chain added (see new configuration parameters).
- Support for IBM PPC750GX added.
- For IBM 750FX/GX, L2 cache content can be displayed via Telnet
- For IBM 750FX/GX, L2 cache coherent memory access implemented, no need for DCACHE FLUSH anymore when L2 cache is active.



Config. program  V1.04  
Firmware for BDI2000  V1.18  
Logic for BDI2000  V1.04



Date: June 9, 2004  
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## Enhancements

- The Telnet command „CONFIG“ allows to change all BDI network parameters. The new parameters are stored in the BDI's flash and a boot sequence is started.
- A new configuration parameter (QACK) allows to force the QACK pin low via the BDI.

## Error Correction

- Programming MirrorBit flash in x16 mode works now also with workspace.
- An error when accessing 750GX L2 cache has been corrected.
- Downloading with workspace works now also for 750GX processors.

Config. program  V1.04  
Firmware for BDI2000  V1.19  
Logic for BDI2000  V1.05



Date: May 20, 2005

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## Enhancements

- The flash unlock command has more features (see user's manual).
- The erase list accepts a new UNLOCK option (see user's manual).
- Telnet history buffer no longer filled with repeated commands.
- The number of supported JTAG devices on the scan chain has been increased to 31.
- Support for MPC83xx (e300) added.

Config. program  V1.05  
Firmware for BDI2000  V1.20  
Logic for BDI2000  V1.05



Date: Sept. 16, 2005  
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## Enhancements

- The Windows setup tools now allows to select a COM port in the range 1 - 19.

## Error Correction

- MPC83xx (e300): Error when debugging with enabled address translation corrected.

Config. program  V1.06  
Firmware for BDI2000  V1.21  
Logic for BDI2000  V1.05



Date: January 13, 2006  
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## Enhancements

- Support for MPC8360 / MPC8358 added.
- Enhanced flash erase support via configuration file (see user's manual).

## Error Correction

- MPC83xx (e300): Error when debug mode is entered with MSR[TGPR] set corrected.

Config. program  V1.06  
Firmware for BDI2000  V1.22  
Logic for BDI2000  V1.05



Date: April 3, 2006

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## Enhancements

- Support for MPC832x (e300c2) added.