

Config. program       V1.02  
Firmware for BDI1000  V1.02  
Logic for BDI1000     V1.01  
RDI DLL                 V1.01



Date: July 28, 2000  
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## Enhancements

- Support for AXD Debugger from ARM Ltd. added (RDI 1.51 Support).
- Support for host communication via network gateway added.

## Error Correction

- Error in fast flash programming algorithm for Intel flash corrected.
- Error in Abort detection for ARM9 targets corrected.

Config. program  V1.02  
Firmware for BDI1000  V1.03  
Logic for BDI1000  V1.01  
RDI DLL  V1.01



Date: Sept 26, 2000  
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## Enhancements

- Flash programming support for Atmel AT49 chips added.
- New flash erase modes support now chip and block erase.

## Error Correction

Config. program  V1.03  
Firmware for BDI1000  V1.04  
Logic for BDI1000  V1.01  
RDI DLL  V1.01



Date: Dec 15, 2000  
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## Enhancements

- Support for multiple devices on the JTAG scan chain added (see new configuration parameters).
- Support for TMS470 added.

## Error Correction

Config. program  V1.03  
Firmware for BDI1000  V1.05  
Logic for BDI1000  V1.01  
RDI DLL  V1.02



Date: Febr 5, 2001  
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## Enhancements

- Support for ARM semihosting added.
- Better power fail handling.

## Error Correction

|                      |   |       |
|----------------------|---|-------|
| Config. program      | ■ | V1.05 |
| Firmware for BDI1000 | ■ | V1.07 |
| Logic for BDI1000    | ■ | V1.02 |
| RDI DLL              | ■ | V1.03 |



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## Enhancements

- Additional JTAG clock frequencies (down to 10kHz) added.
- JTAG clock frequency can be changed with an entry in the init list (see manual).
- A configuration parameter that defines the behavior of the BDI when the debugger attaches has been added (see manual).

## Error Correction

- Error in Thumb mode for ARM9TDMI based targets corrected.
- Correct BYPASS instruction scanned for additional devices on the JTAG scan chain.
- Debugging of ARM940T works now correct also with data cache in write-back mode.
- Big Endian now also supported when using AXD.

Config. program       V1.06  
Firmware for BDI1000  V1.08  
Logic for BDI1000     V1.02  
RDI DLL                 V1.04



Date: October 22, 2001  
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## Enhancements

- Access to CP15 registers via ADW/AXD added.
- For ARM9 targets, the hardware vector catch register is used to implement the semihosting and vector catch feature.
- For ARM9 targets, the debugger's feature to individually catch exception is supported.
- A wake-up delay has been added to support targets that needs some wake-up time after a reset.

## Error Correction

- Error in semihosting function SYS\_GET\_CMDLINE corrected.
- Error in semihosting function SYS\_READ from TTY corrected.

Config. program  V1.06  
Firmware for BDI1000  V1.08  
Logic for BDI1000  V1.02  
RDI DLL  V1.05



Date: January 23, 2002  
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## Enhancements

## Error Correction

- Error when AXD/ADW uses large download blocks corrected.

Config. program  V1.07  
Firmware for BDI1000  V1.09  
Logic for BDI1000  V1.02  
RDI DLL  V1.06



Date: Febr. 11, 2002  
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## Enhancements

- Support for ARM9E, ARM946E and ARM966E added.

## Error Correction

- Error when invalidating 920T instruction cache corrected.
- Error when debugging ARM9 in Thumb mode corrected.



Config. program  V1.07  
Firmware for BDI1000  V1.10  
Logic for BDI1000  V1.02  
RDI DLL  V1.07



Date: April 30, 2002  
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## Error Correction

- Problems during application download corrected.

Config. program      ■ V1.08  
Firmware for BDI1000 ■ V1.11  
Logic for BDI1000    ■ V1.03  
RDI DLL                ■ V1.09



Date: October 29, 2002  
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## Enhancements

- Support for TI925T added.
- With a special init list entry, the TRST diver type can be changed to push-pull (see manual).
- A List of valid memory ranges can be defined to avoid invalid memory accesses via the JTAG interface (MMAP entry in the init list).
- Support for AMD MirrorBit flash added.

## Error Correction

- AMD erase time-out increased.
- Polling a running target does no longer consume almost 100% of host CPU time.

Config. program  V1.10  
Firmware for BDI1000  V1.12  
Logic for BDI1000  V1.03  
RDI DLL  V1.10



Date: April 29, 2003  
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## Enhancements

- Support for 32 bit wide flash chips added.
- New faster download mode added (see WORKSPACE parameter in user's manual).
- Reset sequence changed / optimized.

## Note:

The BDI's ethernet MAC address will change with this release.  
The setup tool displays the new MAC address.

Config. program       V1.11  
Firmware for BDI1000  V1.14  
Logic for BDI1000     V1.03  
RDI DLL                 V1.11



Date: Oct. 28, 2003  
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## Enhancements

- Support for ARM926E added.
- Support for MAC71xx including internal flash programming added.
- Telnet interface added. A Telnet client can connect to the BDI via the network.
- New init list commands added to read from memory.
- The time the BDI asserts the Reset signal can be defined with a special init list entry.

Config. program  V1.11  
Firmware for BDI1000  V1.15  
Logic for BDI1000  V1.04  
RDI DLL  V1.12



Date: January 21, 2004  
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## Enhancements

- JTAG signal timing improved.

## Error Correction

- ARM926E was not accepted as CPU type. This is fixed now.